Document Number: STBV38100G2 Preliminary Datasheet V1.0

STBV38100G2

Gallium Nitride 50V, 100W, 0.1-4.2GHz RF Power Transistor

Description

The STBV38100G2 is a 100watt, GaN HEMT, ideal for general applications from 0.1 to 4.2GHz.

It can support CW, pulse or any modulated signal.

There is no guarantee of performance when this part is used outside of stated frequencies.

Typical Class AB pulse CW performance at different bands

Vds=50V, Idq=100mA

Pulse width=50us, duty cycle=20% (On innogration wideband application board with device soldered)

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Freq	P1dB	P1dB	P1dB	P1dB	P3dB	P3dB	P3dB
(MHz)	(dBm)	(W)	Eff(%)	Gain(dB)	(dBm)	(W)	Eff(%)
3900-4000	48.55	71.6	54.2	16.61	50.37	108.9	61.9
3400-3600	48.66	73.4	52.4	15.82	50.4	109.8	59.2
2110-2170	48.01	63.2	53.5	18.1	50.22	105.2	67.0
1805-1880	47.56	57.0	53.7	19.41	50.07	101.7	67.9
925-960	49.11	81.5	64.5	20.67	50.31	107.3	71.7

Applications

- 5G, 4G wireless infrastructure
- S band power amplifier
- Test instruments
- Jammer

Important Note: Proper Biasing Sequence for GaN HEMT Transistors

Turning the device ON

1. Set VGS to the pinch--off (VP) voltage, typically –5 V

2. Turn on VDS to nominal supply voltage

3. Increase VGS until IDS current is attained

4. Apply RF input power to desired level

Turning the device OFF

- 1. Turn RF power off
- 2. Reduce VGS down to VP, typically -5 V
- 3. Reduce VDS down to 0 V
- 4. Turn off VGS

Table 1. Maximum Ratings

Rating	Symbol	Value	Unit	
DrainSource Voltage	V _{DSS}	+200	Vdc	
GateSource Voltage	V_{GS}	-8 to +0.5	Vdc	
Operating Voltage	V_{DD}	55	Vdc	
Maximum gate current	Igs	12	mA	
Storage Temperature Range	Tstg	-65 to +150	°C	
Case Operating Temperature	T _C	+150	°C	
Operating Junction Temperature	TJ	+225	°C	

Table 2. Thermal Characteristics

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Case by FEA	Po IO	2.2	°C /W
T _C = 85°C, at Pavg=8W WCDMA 1 carrier	Rejc	2.2	-0 /٧٧

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Table 3. Electrical Characteristics (TA = 25℃ unless otherwise noted)

DC Characteristics (measured on wafer prior to packaging)

Characteristic	Conditions	Symbol	Min	Тур	Max	Unit
Drain-Source Breakdown Voltage	VGS=-8V; IDS=12mA	V _{DSS}		200		V
Gate Threshold Voltage	VDS =10V, ID = 12mA	$V_{GS(th)}$	-4	-3	-2	V
Gate Quiescent Voltage	VDS =50V, IDS=120mA, Measured in Functional Test	$V_{GS(Q)}$		-3.04		V

Ruggedness Characteristics

Characteristic	Conditions	Symbol	Min	Тур	Max	Unit
Load mismatch capability	3.8GHz, Pout=100W pulse CW					
	All phase,	VSWR		10:1		
	No device damages					

3.9-4.0GHz

Figure 1: Efficiency and power gain as function of Pout (Measured on application board)

VDD = 50 Vdc, IDQ = 100 mA, Pulse width=20us, duty cycle=20%

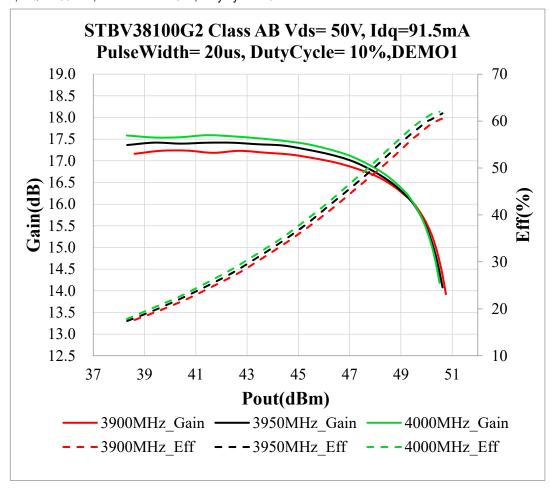
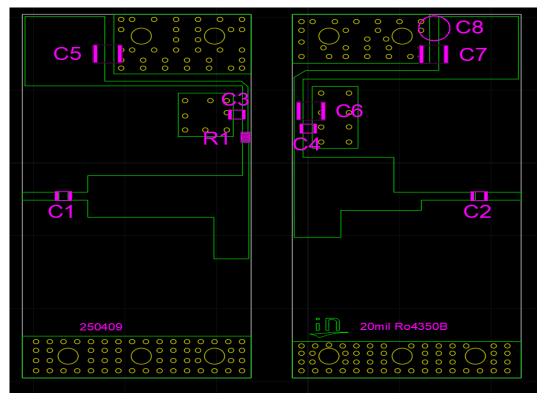




Figure 2: Network plot for S11/S21



Figure 3: Picture of application board





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Table 4. Bill of materials of application board, RO4350B 20Mils (PCB layout upon request)

Component	Value	Quantity
U1	STBV38100G2	1
C1、 C2、C3、C4	8.2pF	4
C5、C6、C7	10uF/63V	3
C8	470uF/63V	1
R1	10 Ω	1



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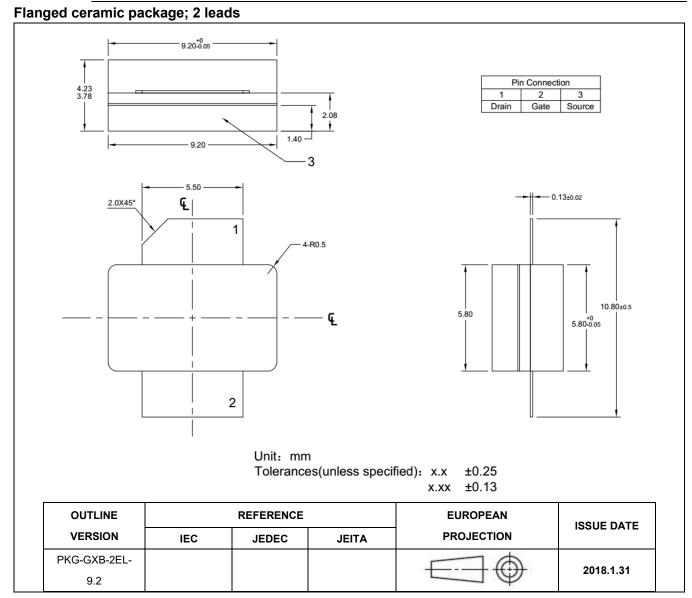


Figure 2. Package Outline PKG-G2



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Revision history

Table 4. Document revision history

Date	Revision	Datasheet Status
2025/7/10	V1.0	Preliminary Datasheet Creation

Application data based on: ZYX-25-21/22/23/24/25

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