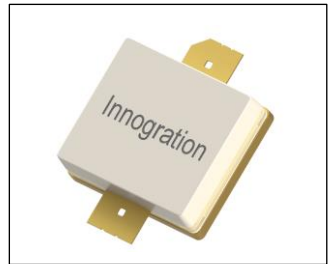




## 150W,L band 28V RF LDMOS Transistor

### Description

The ITGH15150A2C is 100-watt, high performance, Input matched LDMOS transistor, designed for any general applications at frequencies within L band 1.3 to 1.8GHz, in new generation highly cost effective open cavity package.



- Typical 1.4-1.5GHz Class AB RF Performance (On Innegration fixture with device soldered).

<b><math>V_{DS}=28V</math>, <math>V_{peak}=2.66V</math>, <math>I_{DQ}=500mA</math>, Pulsed CW 20us,10%</b>					
Freq (MHz)	P-1(dBm)	P-1Gain (dB)	P-3(dBm)	P-3(W)	EFF (%)
1420	51.40	17.3	52.39	173	53.6
1475	51.22	17.4	52.13	163	56.1
1530	50.75	16.9	51.60	145	58.0

<b><math>V_{DS}=24V</math>, <math>V_{peak}=2.66V</math>, <math>I_{DQ}=500mA</math>, Pulsed CW 20us,10%</b>					
Freq (MHz)	P-1(dBm)	P-1Gain (dB)	P-3(dBm)	P-3(W)	EFF (%)
1420	50.22	17.0	51.17	130	53.8
1475	49.99	17.1	50.84	121	56.2
1530	49.38	16.7	50.22	105	58.2

### Features

- High Efficiency and Linear Gain Operations
- Integrated ESD Protection
- Excellent thermal stability, low HCI drift
- Large Positive and Negative Gate/Source Voltage Range for Improved Class C Operation
- Pb-free, RoHS-compliant

### Suitable Applications

- L band power amplifier

Table 1. Maximum Ratings

Rating	Symbol	Value	Unit
Drain--Source Voltage	$V_{DS}$	+65	Vdc
Gate--Source Voltage	$V_{GS}$	-10 to +10	Vdc
Operating Voltage	$V_{DD}$	+28	Vdc
Storage Temperature Range	$T_{stg}$	-65 to +150	°C
Case Operating Temperature	$T_C$	+150	°C
Operating Junction Temperature	$T_J$	+225	°C

Table 2. Thermal Characteristics

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Case $T_C=85^{\circ}C$ , DC test, <b>device soldered on heatsink directly</b>	$R_{\theta JC}$	0.45	°C/W



**Table 3. ESD Protection Characteristics**

Test Methodology	Class
Human Body Model (per JESD22--A114)	Class 2

**Table 4. Electrical Characteristics** (TA = 25 °C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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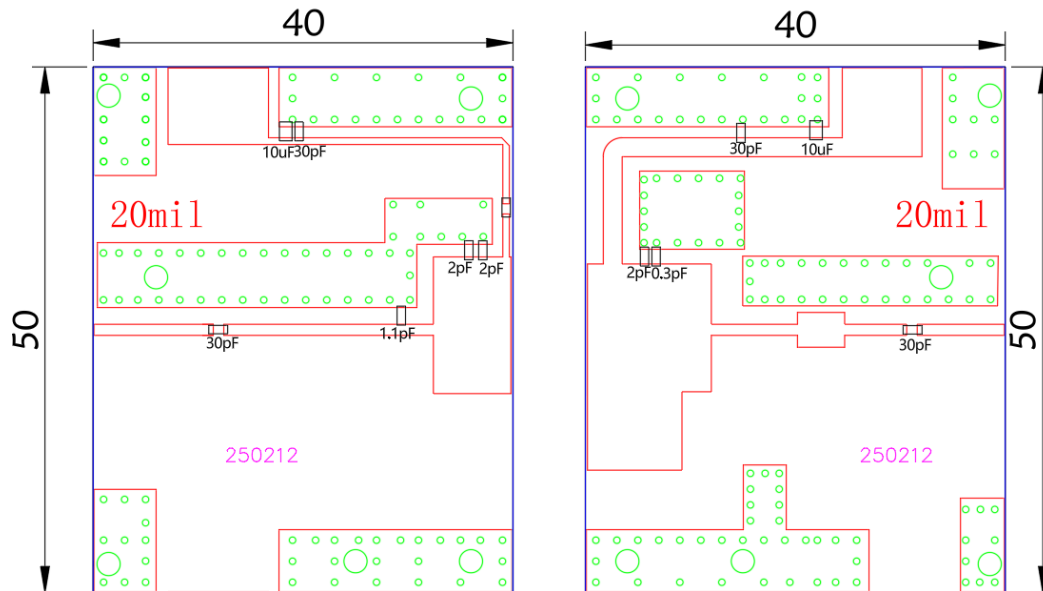
**DC Characteristics DC Characteristics**

Drain-Source Voltage $V_{GS}=0, I_{DS}=100\mu A$	$V_{(BR)DSS}$		65		V
Zero Gate Voltage Drain Leakage Current ( $V_{DS} = 28V, V_{GS} = 0V$ )	$I_{DSS}$	—	—	1	$\mu A$
Gate--Source Leakage Current ( $V_{GS} = 11V, V_{DS} = 0V$ )	$I_{GSS}$	—	—	1	$\mu A$
Gate Threshold Voltage ( $V_{DS} = 28V, I_D = 600\mu A$ )	$V_{GS(th)}$	—	2	—	V
Gate Quiescent Voltage ( $V_{DD} = 28V, I_D = 500mA$ , Measured in Functional Test)	$V_{GS(Q)}$	—	2.7	—	V

**Load Mismatch (In Innegration Test Fixture, 50 ohm system):**  $V_{DD} = 28Vdc, I_{DQ} = 500mA, f = 1400MHz$

VSWR 10:1 at 150W pulse CW Output Power	No Device Degradation
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## 1.4-1.5GHz application board



**Figure 2. Test Circuit Component Layout, 20mils RO4350B**

## TYPICAL CHARACTERISTICS

Figure 3. Power Gain and Drain Efficiency as function of Power Output

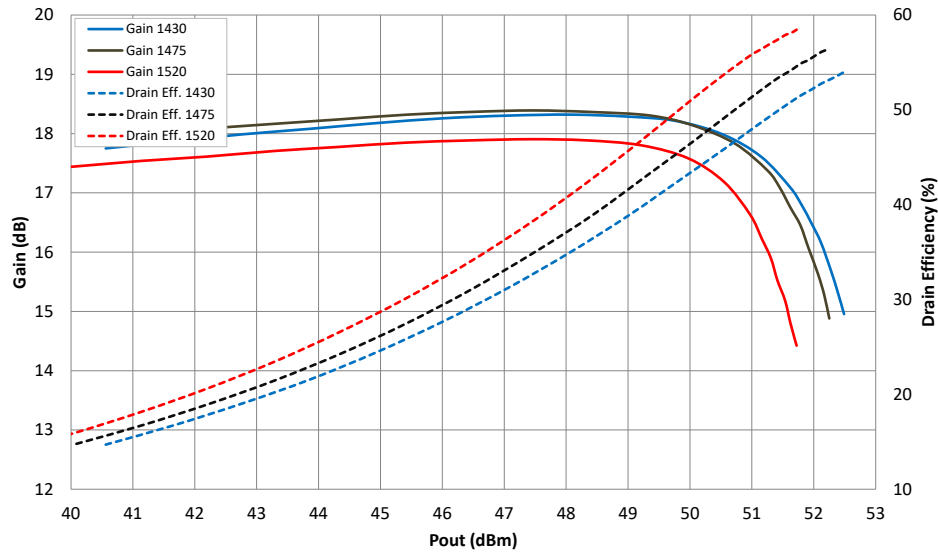
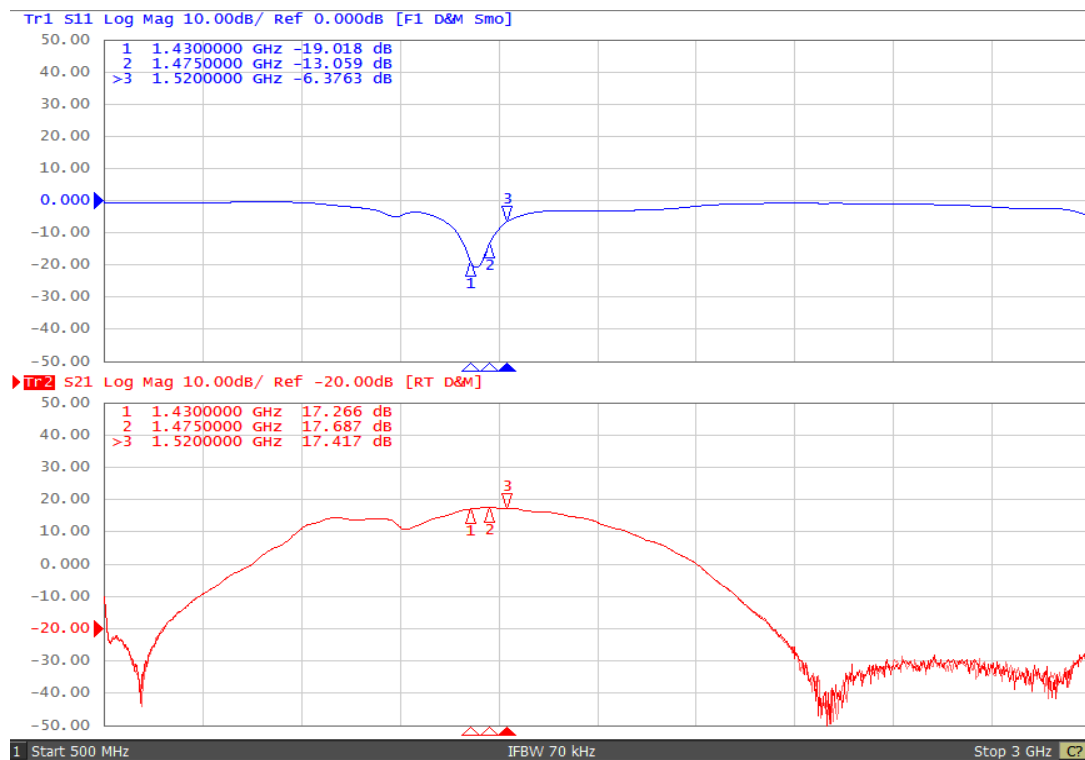
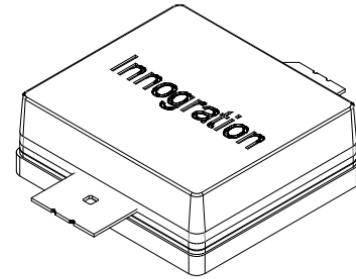
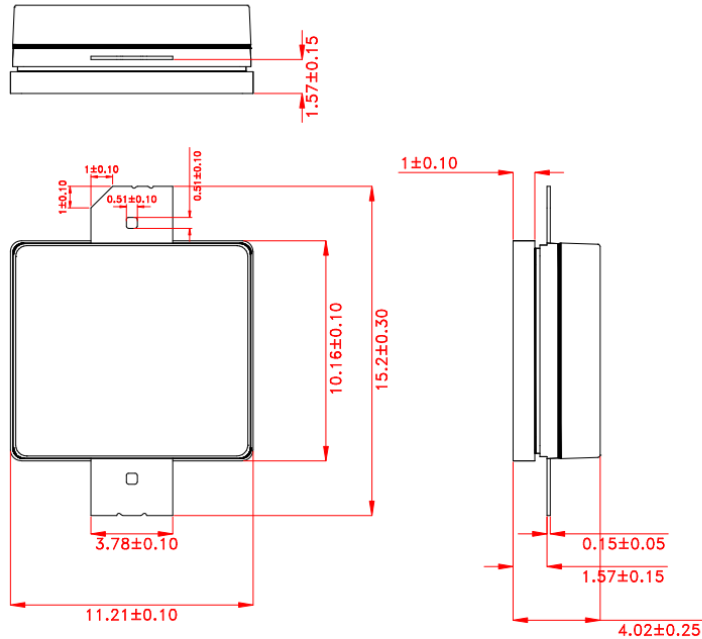


Figure 4: Network analyzer Output S11/S21





## Package Dimensions



Unit:mm

Tolerance  $\pm 0.10$ mm, Except as Noted.

## Revision history

Table 7. Document revision history

Date	Revision	Datasheet Status
2025/8/14	Rev 1.0	Preliminary Datasheet

Application data based on HJ-25-10

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