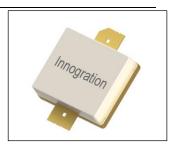
Innogration (Suzhou) Co., Ltd.

Document Number: ITGH09150A2C Preliminary Datasheet V1.0

150W,UHF 28V RF LDMOS Transistor

Description

The ITGH09150A2C is 150-watt, high performance, Input matched LDMOS transistor, designed for any general applications at frequencies within UHF band, particularly 915MHz, in new generation highly cost effective open cavity package.



Typical 915MHz Class AB CW RF Performance (On Innogration fixture with device soldered).

$V_{DS} = 28V$, $I_{DQ} = 5mA$

Freq	P1dB	P1dB	P1dB	P1dB	P3dB	P3dB	P3dB
(MHz)	(dBm)	(W)	Eff(%)	Gain(dB)	(dBm)	(W)	Eff(%)
915	51.1	128.8	70.6	16. 51	51.89	154.4	74.0

Features

- High Efficiency and Linear Gain Operations
- Integrated ESD Protection
- Excellent thermal stability, low HCI drift
- Large Positive and Negative Gate/Source Voltage Range for Improved Class C Operation
- Pb-free, RoHS-compliant

Suitable Applications

- UHF band power amplifier
- 915MHz ISM

Table 1. Maximum Ratings

Rating	Symbol	Value	Unit
DrainSource Voltage	V _{DSS}	+65	Vdc
GateSource Voltage	V_{GS}	-10 to +10	Vdc
Operating Voltage	V_{DD}	+28	Vdc
Storage Temperature Range	Tstg	-65 to +150	°C
Case Operating Temperature	Tc	+150	°C
Operating Junction Temperature	TJ	+225	°C

Table 2. Thermal Characteristics

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Case	RеJC	0.45	°C/W
T _C = 85°C, DC test, device soldered on heatsink directly		0.45	

Table 3. ESD Protection Characteristics

Test Methodology	Class	
Human Body Model (per JESD22A114)	Class 2	

Table 4. Electrical Characteristics (TA = 25 °C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
DC Characteristics DC Characteristics					
Drain-Source Voltage			C.F.		V
V _{GS} =0, I _{DS} =100uA	V (BR)DSS		65		V



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Zero Gate Voltage Drain Leakage Current	I _{DSS}	 	1	μА
$(V_{DS} = 28V, V_{GS} = 0 V)$				•
GateSource Leakage Current		 	1	^
$(V_{GS} = 11 \text{ V}, V_{DS} = 0 \text{ V})$	Igss	 -	ı	μΑ
Gate Threshold Voltage	V (th)	2		V
$(V_{DS} = 28V, I_D = 600 \mu A)$	V _{GS} (th)	 2		V
Gate Quiescent Voltage	V	 2.7		V
(V _{DD} = 28V, I _D = 500mA, Measured in Functional Test)	$V_{GS(Q)}$	 2.1		V

Load Mismatch (In Innogration Test Fixture, 50 ohm system): $V_{DD} = 28Vdc$, $I_{DQ} = 500$ mA, f = 9150 MHz

VSWR 10:1 at 150W pulse CW Output Power

No Device Degradation

915MHz application board

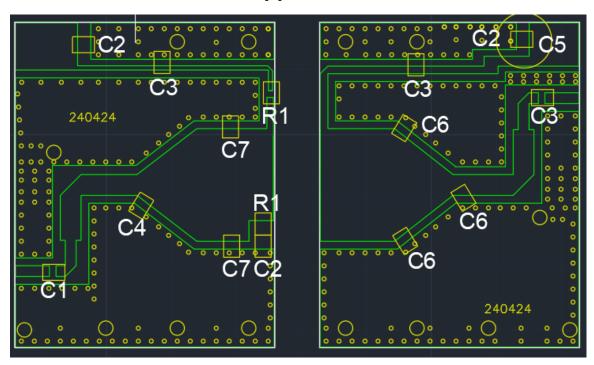


Figure 2. Test Circuit Component Layout, 30mils RO4350B

вом				
Component	Value	Quantity		
C1	3.9pF	1		
C2	10uF	3		
R1	10 ohm	2		
C5	470uF	1		
C3	47pF	3		
C4	1pF	1		
C6	5.6pF	3		
C7	4.3pF	2		



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TYPICAL CHARACTERISTICS

Figure 3. Power Gain and Drain Efficiency as function of Power Output

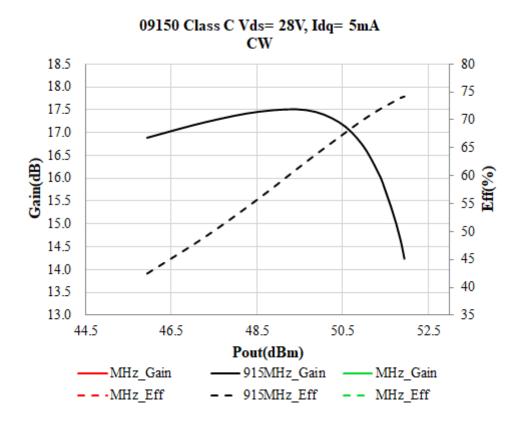
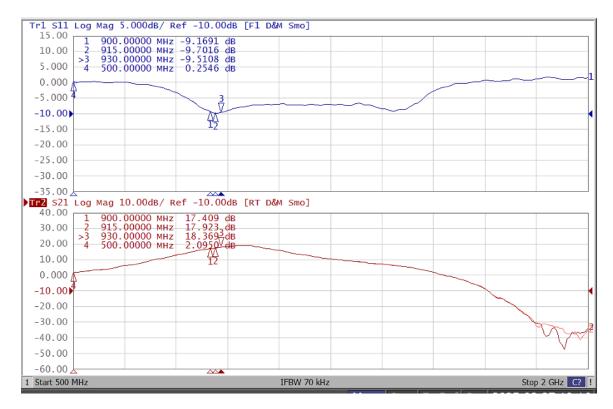
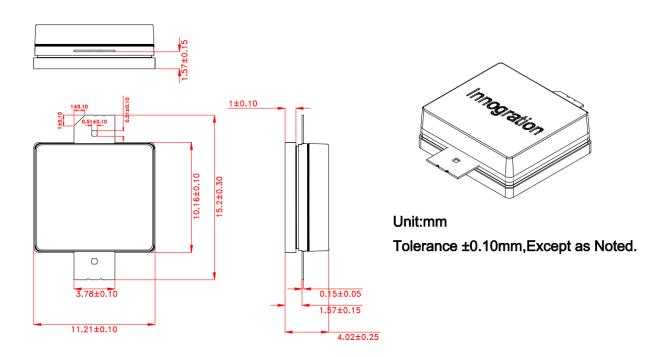


Figure 4: Network analyzer Output S11/S21 (Vds=28V, Idq=500mA)





Package Dimensions



Revision history

Table 7. Document revision history

Date	Revision	Datasheet Status
2025/8/27	Rev 1.0	Preliminary Datasheet

Application data based on ZXY-25-11

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