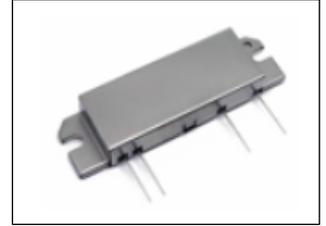


## 136-174M,60W, 12.5V, 2 stage for Mobile radio

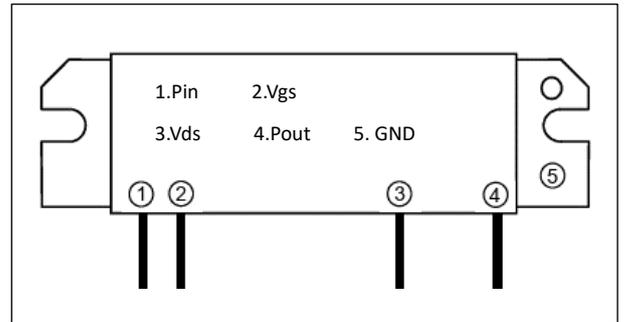
### Description

IA60N1317 is a rugged 60W RF LDMOS Amplifier Module for 12.5V mobile radios that operate in the 136 to 174MHz range. The battery can be connected directly to the drain of the modules. This module is designed for non-linear modulation, but may also be used for linear modulation by setting the drain quiescent current with the gate voltage and controlling the output power with the input power.



### Features

- Rugged LDMOS technology
- $P_{out} > 60W$ ,  $Eff > 45\%$  @  $V_{ds} = 12.5V$ ,  $V_{gs} = 5V$ ,  $P_{in} = 50mW$
- Broadband Frequency Range: 136-174MHz
- Metal shielding structure
- Module Size: 67 x 19.4 x 9.9 mm
- DC block capacitor integrated
- Linear and non linear operation supported



**Table 1. Maximum Ratings**

Rating	Symbol	Value	Unit
Drain--Source Voltage	$V_{DSS}$	+65	Vdc
Gate--Source Voltage	$V_{GS}$	-10 to +10	Vdc
Operating Voltage	$V_{DD}$	+24	Vdc
Storage Temperature Range	$T_{stg}$	-65 to +150	°C
Case Operating Temperature	$T_c$	+150	°C
Operating Junction Temperature	$T_J$	+225	°C

**Table 2. ELECTRICAL CHARACTERISTICS ( $T_{case} = +25^{\circ}C$ ,  $Z_G = Z_L = 50\Omega$ , unless otherwise specified)**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
f	Frequency Range	-	136	-	174	MHz
$P_{out}$	Output Power	$V_{DD} = 12.5V$ , $V_{GG} = 5V$ , $P_{in} = 50mW$	60	-	-	W
$\eta$	Total Efficiency		45	-	-	%
$2f_0$	2nd Harmonic		-	-	-11	dBc
$3f_0$	3rd Harmonic		-	-	-30	dBc
$in$	Input VSWR		-	-	2:1	-
$I_{DD}$	Leakage Current	$V_{DD} = 17V$ , $V_{GG} = 0V$ , $P_{in} = 0W$	-	-	1	mA
-	Load VSWR Tolerance	$V_{DD} = 15.2V$ , $P_{in} = 50mW$ , $P_{out} = 30W$ ( $V_{GG}$ adj.), Load VSWR=20:1 (All phase)	No degradation or destroy			-
-	Stability	$V_{DD} = 10/12.5/15.2V$ , $P_{in} = 25/50/70mW$ , $P_{out} \leq 40W$ ( $V_{GG}$ control), Load VSWR=3:1 (All phase)	No parasitic oscillation more than -60dBc			-

Figure 1: Network analyzer Output S11/S21

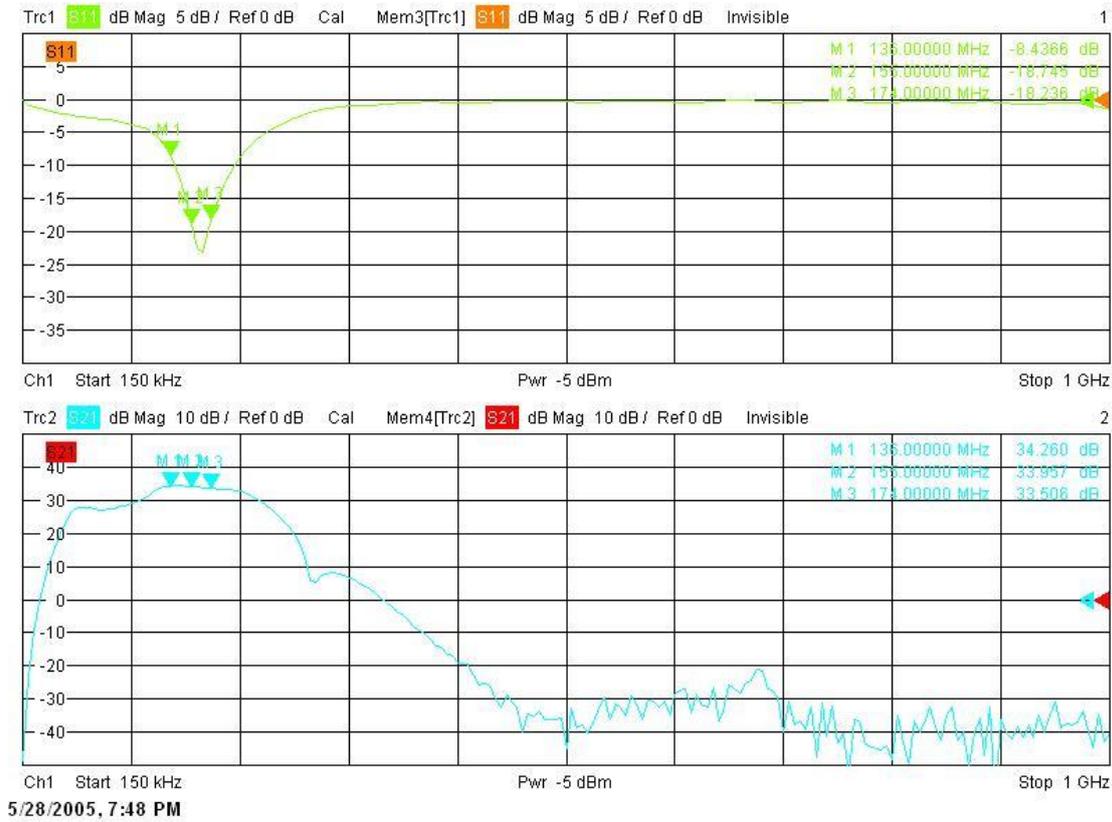
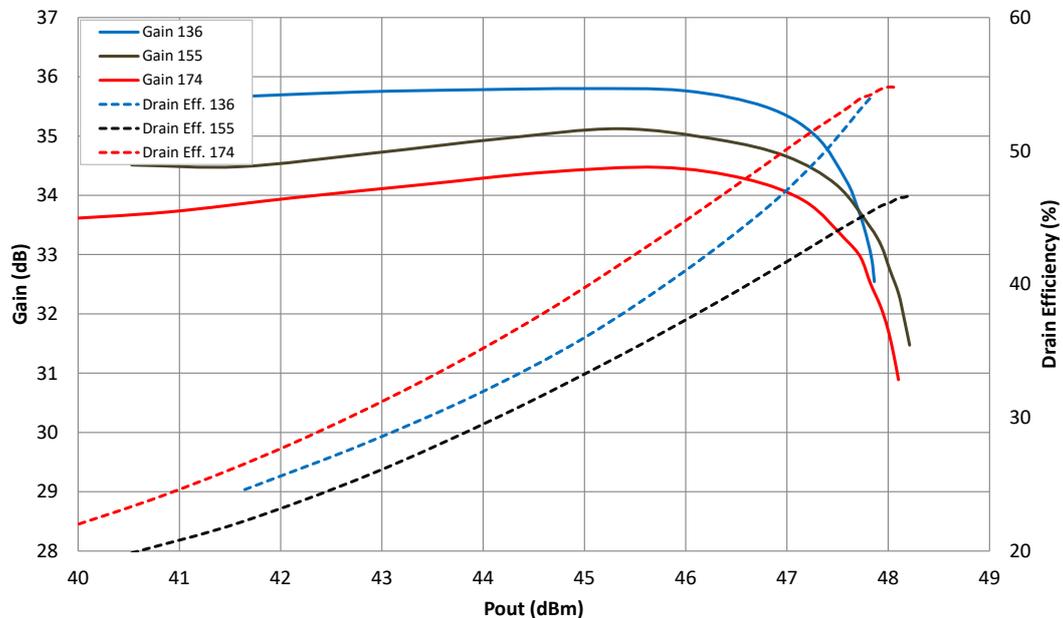


Figure 2: CW Power gain, Efficiency as function of output Power

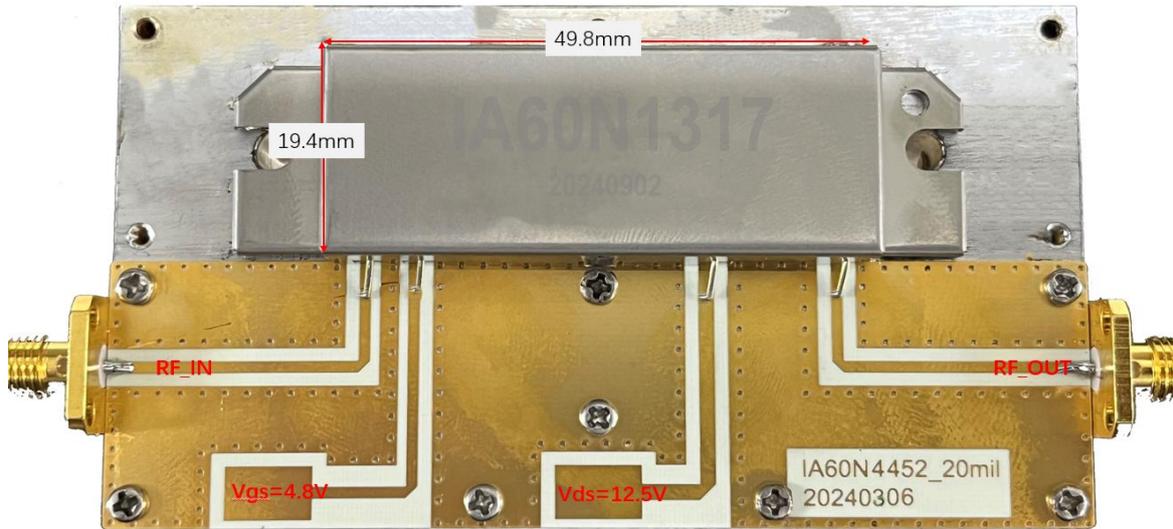


# IA60N1317 PA module

Document Number: IA60N1317  
Preliminary Datasheet V1.0

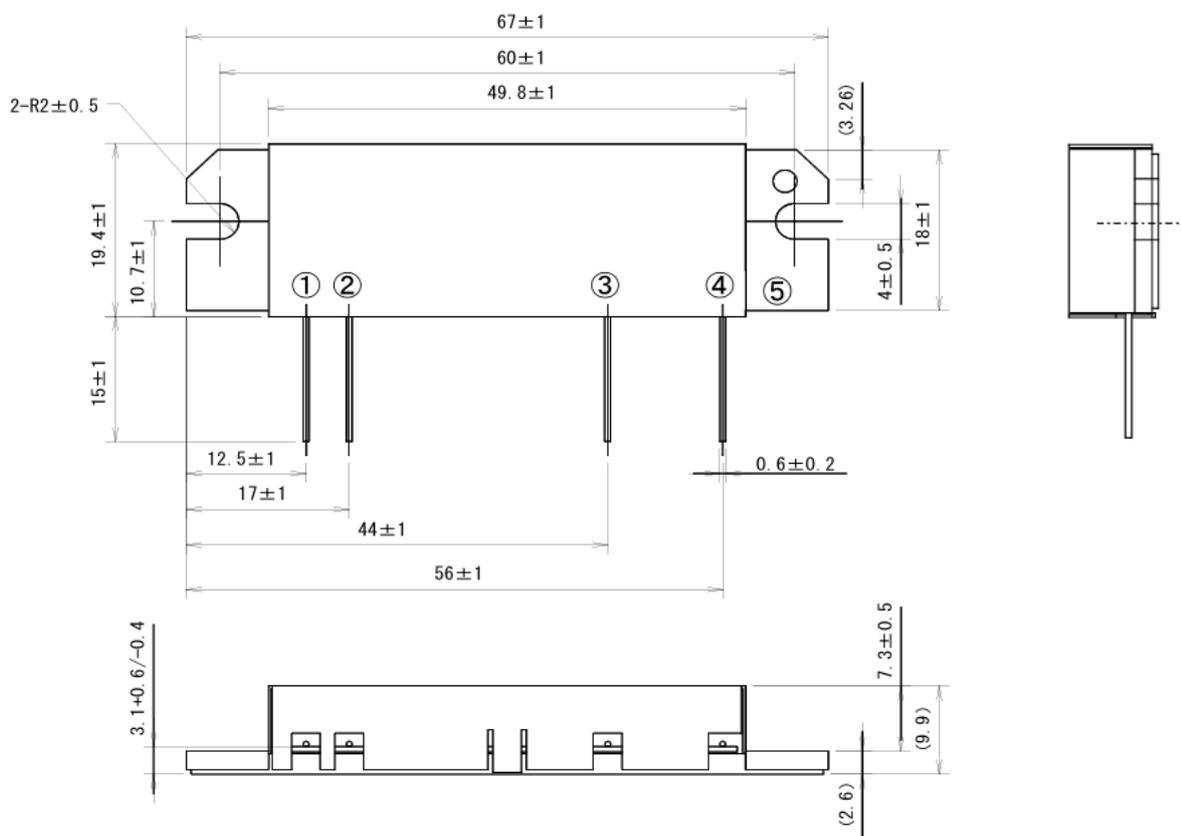
<b><math>V_{DS}= 12.5V, V_{gs}= 4.8V, I_{dq}=900mA</math></b>					
Freq (MHz)	P1(dBm)	P1 Gain ( dB )	P3dB(dBm)	P3dB(W)	EFF (%)
136	47.36	34.8	47.90	61.6	55.1
155	47.51	34.1	48.13	65.0	46.5
174	47.43	33.5	48.03	63.6	54.8

Figure 3: Test circuit



## Package Outline

Flanged package; 2 mounting holes; 4 Pins, Unit:mm



OUTLINE VERSION	REFERENCE			EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA		
PKG-					06/17/2024

## Revision history

Table 5. Document revision history

Date	Revision	Datasheet Status
2024/9/2	Rev 1.0	Preliminary Datasheet Creation

Application data based on HJ-24-15

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